



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Reply, Please Refer to the Application of:

Application No.: 10/633,373

Filed: July 31, 2003

For: **Fiber Channel Adaptor for Serial or
Parallel ATA Disks**

Art Group: 2182

Examiner: Jasjit S. Vidwan

Confirmation No.: 5120

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed), which are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).


The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 7/27/2007



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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

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of

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Complete if Known

Application Number	10/633,373
Filing Date	July 31, 2003
First Named Inventor	
Art Unit	2182
Examiner Name	Jasjit S. Vidwan
Attorney Docket Number	5693P290X

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-4,075,691	02-21-1978	Davis et al.	
		US-4,351,023	09-21-1982	Richer	
		US-4,710,868	12-01-1987	Cocke et al.	
		US-5,088,081	02-11-1992	Farr	
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		US-5,134,619	07-28-1992	Henson et al.	
		US-5,155,835	10-13-1992	Belsan	
		US-5,235,601	08-10-1993	Stallmo et al.	
		US-5,379,417	01-03-1995	Lui et al.	
		US-5,555,244	09-10-1996	Gupta et al.	
		US-5,668,943	09-16-1997	Attanasio et al.	
		US-5,950,203	09-07-1999	Stakuis et al.	
		US-5,996,086	11-30-1999	Delaney et al.	
		US-6,444,902	09-03-2002	Tsao et al.	
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ² - Number ⁴ - Kind Code ⁵ (if known)				
		EP 0 569 313 A2	11-10-1993	Styczinski		
		EP 0 760 503 A1	03-05-1997	Hemphill et al.		
		WO 01/14991 A2	03-01-2001	Kleiman		
		WO 01/43368 A1	06-14-2001	Kleiman		
		EP 0 939 360 A2	02-24-1999	Okada		

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
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				Art Unit	2182
				Examiner Name	Jasjit S. Vidwan
Sheet	2	of	2	Attorney Docket Number	5693P290X

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		IBM Technical Disclosure Bulletin, "Fibre Channel Standard Hub-Loop Redundancy for Higher RAS," Volume 37, No. 04A, April 1994, XP000446711.		
		KLEIMAN et al.: "Using NUMA Interconnects For Highly Available Filers," 1999 IEEE.		
		NASS: "Connect Disk Arrays to EISA or PCI Buses," Electronic Design, November 11, 1993, Cleveland, OH, US. XP000417908.		

Examiner Signature		Date Considered	
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